

## **Electric double layer effect in the vicinity of solid electrolyte/diamond interfaces and the application to neuromorphic computing**

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### **Abstract**

Electric double layer effect in solid electrochemical systems is a key topic in energy storage applications. In this review, we outline recent investigations on the electric double layer effect of solid electrolyte interfaces by utilizing a semiconducting diamond surface as a probe of electrical charges at the solid/solid interfaces. Hall measurements with various solid electrolyte-based transistors evidenced that the electric double-layer effect strongly depends on the properties of the electrolyte and the very thin region from the interface. The unveiled features of the electric double layer at solid electrolyte interfaces are quantitatively discussed from the viewpoint of charge density and charging-discharging rate. Furthermore, applications of the unique switching response of the electric double layer transistors to neuromorphic computing are also demonstrated.

### **1. Introduction**

Colossal energy consumption and carbon emissions are becoming a severe problem in many parts of the world, and a shift to electric vehicles is underway to solve the problem. Developing technologies related to electric vehicles is essential, and developing high-power, all-solid-state batteries is particularly urgent. All-solid-state batteries consist entirely of solid materials for electrodes and electrolytes and have advantages over lithium-ion batteries, which use liquid electrolytes in terms of high output and high safety with no risk of ignition, which has led to intensified research competition worldwide.<sup>1</sup> On the other hand, the output of all-solid-state

batteries is currently not sufficiently high, and one of the main reasons for this is the high interface resistance. Various factors have been discussed concerning the high interfacial resistance, among which the interfacial phenomenon known as the electric double-layer effect or space-charge-layer effect has attracted much attention.<sup>2-6</sup> The electric double-layer effect is caused by the distribution of ion concentration inside the electrolyte and electrode,<sup>7</sup> and corresponds to the depletion layer and Schottky barrier in the semiconductor field, which are known to strongly influence the interfacial resistance of the electron current across the electrode/semiconductor interface,<sup>8</sup> and is therefore suggested to have a significant effect on interfacial resistance in all-solid-state batteries as well. It is suggested that they impact the interface resistance in all-solid-state batteries. Several cases have been reported in which the dominant contribution of the electric double layer or space charge layer to the lithium ion transfer resistance at the solid electrolyte/electrode interface is suspected to change the battery output by several orders of magnitude. Investigation of the electric double layer or space charge layer at the solid electrolyte interface is an urgent task.<sup>3-5</sup> However, while the electric double layer or space charge layer has been investigated in great detail in electrochemical systems with liquid electrolytes, it is still at a fundamental stage in electrochemical systems with solid electrolytes and is poorly understood, including its existence. The reason for this lack of understanding of the electric double layer in solid systems is that solid electrolytes tend to be ionic and electronic mixed conductors due to electron conduction caused by internal charge compensation in conjunction with ionic transport,<sup>9-12</sup> and the resulting chemical capacitance complicates the system, making detailed analysis using conventional electrochemical measurements difficult. This is due to the difficulty of detailed analysis by conventional electrochemical measurements. Therefore, new experimental methods that do not rely on conventional methods are needed to investigate the electrochemical double-layer effects in solid electrolyte systems.

We have recently pioneered a novel method to probe the electric double layer effect at the solid electrolyte interface.<sup>13</sup> This method, inspired by the field-effect transistor principle in semiconductor science, utilizes chemically inert diamond as the semiconductor channel material. By creating an ion-blocking interface that hinders ion transfer between the solid electrolyte and semiconductor channel, we were able to quantitatively assess the electric double layer effects occurring at this interface and the modulation behavior induced by applying an external voltage through Hall measurements. These investigations unveiled the existence of the electric double layer effect at the solid electrolyte interface and its strong dependence on the material. We also discovered that the charge-discharge rate of such an electric double layer varies over several orders of magnitude under the dominant influence of the material and composition in the immediate vicinity of the solid electrolyte/semiconductor

channel interface.<sup>14</sup> Moreover, we have recently demonstrated the application of electric double-layer transistors using solid electrolytes in high-efficiency information processing, specifically neuromorphic computing, a burgeoning field in electrical information, with exceptional performance.<sup>14-16</sup> This article presents the recent strides in our fundamental and applied research on the electric double-layer effect near the solid electrolyte interface.

## **2. Evaluation of electric double layer charge near various solid electrolyte/diamond interfaces.**

This study utilized the field-effect transistor (FET) operation mechanism usually used in electronic circuits built into electronic products.<sup>8</sup> FET has a structure in which a dielectric oxide thin film (e.g., SiO<sub>2</sub>, Al<sub>2</sub>O<sub>3</sub>) is inserted between a gate electrode and a semiconductor channel (e.g., p-Si, n-Si).<sup>8</sup> Application of a voltage between gate and source electrodes (i.e., gate voltage,  $V_G$ ) injects an electric charge on the surface of the semiconductor channel, resulting in the wide modulation of the electronic carrier density (e.g., electron, hole) and controlling the electronic current between the source and drain electrodes (i.e., semiconductor channel).<sup>8</sup> Whereas insulators without a trace of ion conduction are used for the dielectric part of FETs in conventional electronic circuits, a lithium solid electrolyte thin film was used for the dielectric part of the FET in this study. Furthermore, an ion-blocking property of diamond, in which lithium ions do not react, was employed for the semiconductor part.<sup>17,18</sup> Thanks to the FET structure, we can selectively detect electric double layer charges on the surface of the electrode (semiconductor channel) formed in the electric double-layer mechanism with various solid electrolytes. First, we investigated the electric double layer effect using two different Li<sup>+</sup> solid electrolytes, Li-Si-Zr-O (LSZO) and La-Li-Ti-O (LLTO) thin films, which have comparable Li<sup>+</sup> conductivity, though the film composition (elements) is different. Both solid electrolyte thin films have an amorphous nature, which was obtained by room temperature deposition via pulsed laser deposition method with a UV excimer laser (a wavelength of 193 nm). We fabricated three transistors termed LSZO, LLTO, and LLTO/LSZO transistors, as shown in Fig.1, on the surface of the flat surface of a hydrogen-terminated diamond single crystal with lithography patterned electrodes. While LSZO and LLTO transistors are diamond-based transistors with LSZO or LLTO films, LLTO/LSZO transistor is LSZO transistor with a 5 nm LLTO interlayer inserted at the diamond/LSZO interface, meaning that the Li<sup>+</sup> electrolyte at the interface is not LSZO but LLTO.

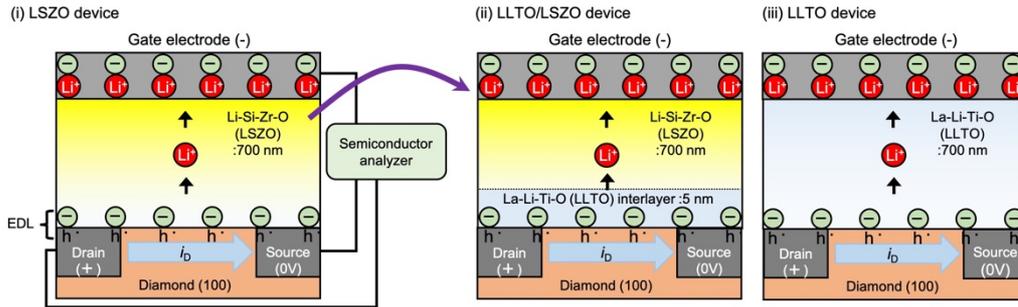
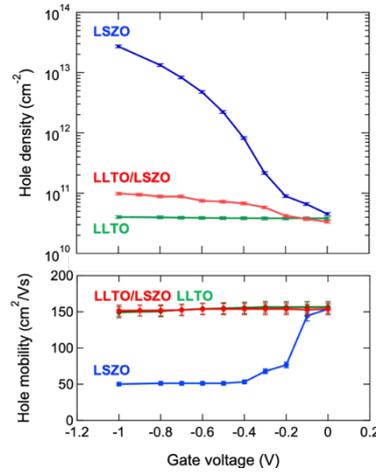


Fig. 1. Illustrations of three diamond-based electric double layer transistors consisted of LSZO or LLTO  $\text{Li}^+$  solid electrolyte thin films.<sup>13</sup> Reproduced from [13] by The Author(s) licensed under CC BY 4.0, published by Springer Nature.

**Figure 2** shows the  $V_G$  dependence of the hole density measured by Hall measurements.<sup>13</sup> The  $V_G$  polarity is positive in that  $\text{Li}^+$  are removed more out of the hydrogen-terminated diamond/ $\text{Li}^+$  solid electrolyte interface. As the  $V_G$  is increased in the positive polarity, the hole density on the diamond surface changes by about three orders of magnitude from  $4 \times 10^{10} \text{ cm}^{-2}$  to  $2 \times 10^{13} \text{ cm}^{-2}$  due to the electric double layer effect in the LSZO transistor. The result evidenced that electric double layer effect exists even with a solid electrolyte system. In contrast to the LSZO transistor, the LLTO transistor showed no change in the hole density in the  $V_G$  range. In addition, the LLTO/LSZO transistor with a 5 nm thick LLTO interlayer also showed very slight carrier density changes similar to the LLTO transistor. The clear difference between the two groups, one is LSZO transistor, and the other is LLTO and LLTO/LSZO transistors, indicates that the electric double layer effect accompanied by hole density modulation is hindered at the LLTO thin film interface, in contrast to the LSZO thin film interface. These results further indicate that the property of electric double layer at the solid electrolyte interface is strongly influenced by the electrolyte composition in a very thin region from the semiconductor/solid electrolyte interface.



**Fig. 2.**  $V_G$  dependence of hole density and mobility for LSZO, LLTO and LLTO/LSZO transistors.<sup>13</sup> Reproduced from [13] by The Author(s) licensed under CC BY 4.0, published by Springer Nature.

To investigate the origin of the suppression of the electric double layer effect of the LLTO thin film observed in the Hall measurement of **Fig. 2**, in situ scanning transmission electron microscopy electron energy-loss spectroscopy (STEM-EELS) observations were carried out.<sup>13</sup> A two-terminal cell with a diamond electrode/LLTO thin film (5 nm)/LSZO thin film/LiCoO<sub>2</sub> electrode shown in **Fig. 3(a)** was fabricated, and the Li *K*-edge and Ti *L*-edge EEL spectra near the diamond electrode/LLTO thin film interface were measured while applying a DC voltage. The voltage direction is positive in that lithium ions are drawn more from the diamond/solid electrolyte interface. The depth profiles of lithium ions obtained using the integrated intensity of the Li *K*-edge EEL spectra are shown in **Fig. 3(b)**. At the LLTO/LSZO interface, Li<sup>+</sup> transfer/transport is driven by a gradient of electrochemical potential of Li<sup>+</sup>,  $\tilde{\mu}_{\text{Li}^+} = \mu_{\text{Li}^+} + F\phi$  until  $\tilde{\mu}_{\text{Li}^+}$  for the LLTO thin film ( $\tilde{\mu}_{\text{Li}^+, \text{LLTO}}$ ) becomes equal to the one for LSZO thin film ( $\tilde{\mu}_{\text{Li}^+, \text{LSZO}}$ ). Even under the condition that there is no  $\phi$  gradient due to  $V=0\text{V}$  and thus  $\mu_{\text{Li}^+, \text{LLTO}} = \mu_{\text{Li}^+, \text{LSZO}}$ , Li<sup>+</sup> concentration ( $[\text{Li}^+]$ ) can differ between the LLTO and LSZO layers since  $\mu_{\text{Li}^+} (= \mu_{\text{Li}^+}^\circ + RT \ln \gamma_{\text{Li}^+} + RT \ln [\text{Li}^+])$  includes terms of a standard chemical potential of Li<sup>+</sup> ( $\mu_{\text{Li}^+}^\circ$ ) and an activity coefficient ( $\gamma_{\text{Li}^+}$ ), which are unique to LLTO and LSZO respectively. The distribution state of Li ions at  $V=0\text{V}$  can be understood in the mechanism. Figure 3(b) shows that the lithium ion concentration in the LLTO and LSZO thin films was significantly lower at  $V=1\text{V}$  compared to the depth profiles at  $V=0\text{V}$ . From this result, it was confirmed that the insertion of LLTO thin film did not hinder the transport of lithium ions and that changes in the concentration of lithium ions occurred.

The Ti *L*-edge EEL spectrum measured in the LLTO thin film region under similar

voltage application conditions is shown in **Fig. 3(c)**: the spectral shape observed at  $V=0$  V consists of the  $L_2$  and  $L_3$  peaks and is in good agreement with the spectral shape widely observed in materials in which the Ti ion shows +4 valence.<sup>19</sup> However, a slight shift towards the low energy loss side is observed due to the contribution of the +3 valence, which is located on the lower energy loss side than the +4 valence. On the other hand, in the spectrum observed at  $V=1$  V, both  $L_2$  and  $L_3$  peaks shifted towards the high energy loss side. This indicates that the excess negative charge generated by the lithium ions being pulled out of the LLTO thin film by applying positive voltage is compensated by the oxidation reaction of Ti ions from +3 to +4 valence. In other words, the suppression of hole density change observed in the hole measurement of the transistor using the LLTO thin film is considered to be caused by the charge compensation by the redox reaction of Ti ions in the LLTO thin film. Therefore, the contrasting differences observed between LSZO and LLTO electrolytes can be understood by considering charge compensation inside the electrolyte.

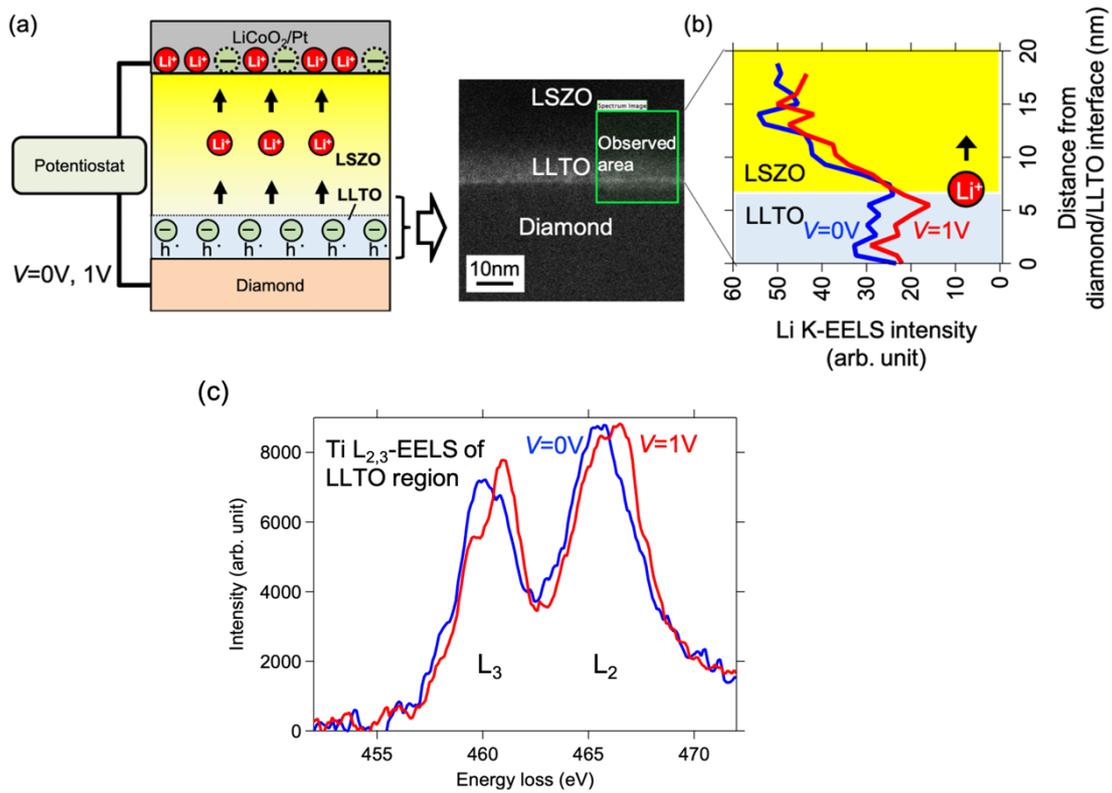
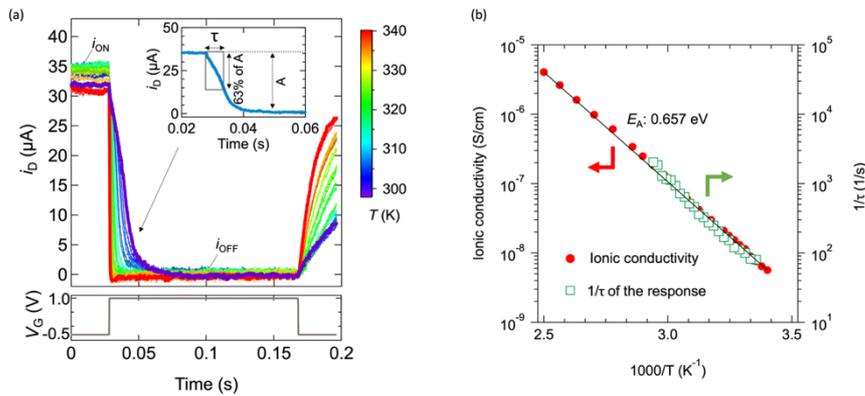


Fig.3 (a) 2-terminal cell for STEM-EELS. (b) depth distribution of lithium ions obtained from Li  $K$ -edge EEL spectra. (c) Ti  $L$ -edge EEL spectra measured in the LLTO region.<sup>13</sup> Reproduced from [13] by The Author(s) licensed under CC BY 4.0, published by Springer Nature.

### 3. Control of electric double layer charge-discharge dynamics by using thin interlayer

The previous section reviewed investigations of electric double layer charges at solid electrolyte interfaces under steady state conditions. The information is useful for discussing the ability of solid electrolytes to form electric double layer charges at the interfaces with a specific electrode or channel material. On the other hand, the investigation mainly done with Hall measurements gives no information on the charging and discharging rate of the electric double layer. In this section, investigations of real-time dynamics of the electric double layer with transistors consisting of various solid electrolytes were done by measuring transient drain current ( $i_D$ ) response under pulsed  $V_G$  applied conditions.

**Figure 4(a)** shows the  $i_D$  transient response of the LSZO transistor measured at various temperatures. The  $i_D$  response between  $i_{ON}$  and  $i_{OFF}$  becomes fast as the temperature rises.<sup>13</sup> The inset of **Fig. 4(a)** shows the definition of the time constant  $\tau$  of the  $i_D$  response as the time for  $i_D$  to decrease to  $1/e$  (i.e. 37%) of the initial  $i_D$  value. Specifically,  $\tau$  is 12 ms at 298 K and 490  $\mu$ s at 340 K. **Figure 4(b)** shows an Arrhenius-type plot of  $1/\tau$ . The ionic conductivity of the LSZO thin film is also plotted in the figure for comparison. Arrhenius-type thermal activation with an activation energy ( $E_A$ ) of about 0.657 eV was observed in  $1/\tau$  and ionic conductivity. This supports that the ionic conductivity of the LSZO strongly affects the  $i_D$  switching response.

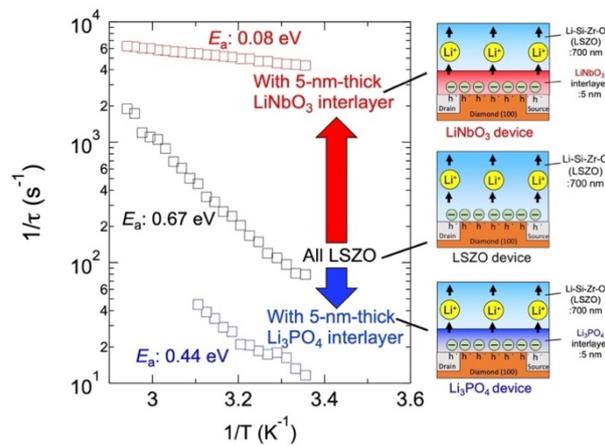


**Fig. 4** (a)  $i_D$  transient response of the LSZO transistor. (b) Arrhenius-type plot of ionic conductivity of  $1/\tau$  and LSZO.<sup>13</sup> Reproduced from [13] by The Author(s) licensed under CC BY 4.0, published by Springer Nature.

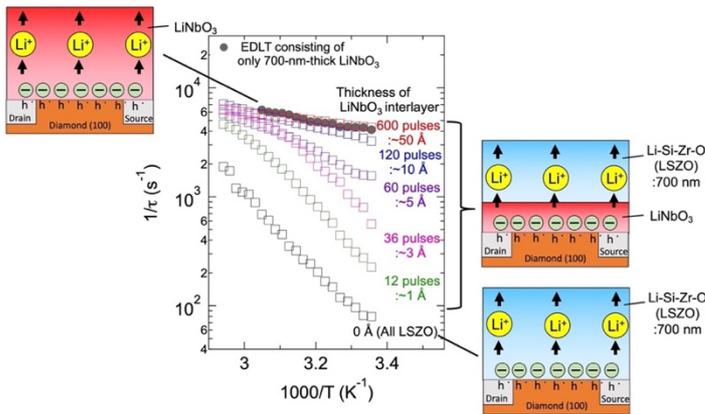
We tackled to tune the response by insertion of another two types of  $\text{Li}^+$  solid electrolyte at the LSZO/diamond interface of the LSZO transistor.<sup>20</sup> 5 nm thick amorphous  $\text{LiNbO}_3$  and amorphous  $\text{Li}_3\text{PO}_4$  films deposited by pulsed laser deposition were inserted at the LSZO/diamond interface as an interlayer. With the transistors, the temperature

dependence of the  $i_D$  transient response was measured as shown in the Arrhenius-type plot of  $1/\tau$  in Fig. 5.20. The result evidences that the insertion of the thin interlayer has a huge impact on the  $i_D$  response speed.<sup>20</sup> Specifically,  $1/\tau$  of the  $\text{LiNbO}_3$  transistor was 4132 ( $\tau = 229 \mu\text{s}$ ) at 298 K, more than ten times faster than the LSZO device ( $\tau = 12.5 \text{ ms}$ ), while  $1/\tau$  of the  $\text{Li}_3\text{PO}_4$  transistor was 11.6 ( $\tau = 61.4 \text{ ms}$ ), which is significantly slower than the LSZO transistor. From the above, it is concluded that the  $\text{LiNbO}_3$  or  $\text{Li}_3\text{PO}_4$  interlayer has excellent control over the  $i_D$  response speed.<sup>20</sup>

An Arrhenius-type plot of  $1/\tau$  in LSZO devices with  $\text{LiNbO}_3$  interlayer of various thicknesses is shown in Fig. 6.<sup>20</sup> No notable variation in  $1/\tau$  on the thickness of  $\text{LiNbO}_3$  interlayer was observed when the thickness of the  $\text{LiNbO}_3$  interlayer was reduced from 5 nm to 1 nm. However, as the thickness of the  $\text{LiNbO}_3$  interlayer decreased from 1 nm to 1 Å, the  $i_D$  transient response rate decreased significantly and asymptotically approached the  $1/\tau$  of LSZO devices. In this region, the value of  $1/\tau$  and the apparent activation energy depended on the film thickness. These results suggest that the electric double layer's charge-discharge properties are under the electrolyte's dominant influence in the range of about 5 Å from the diamond interface.



**Fig. 5** Arrhenius-type plot of  $1/\tau$  for LSZO,  $\text{LiNbO}_3$ , and  $\text{Li}_3\text{PO}_4$  devices.<sup>20</sup> Copyright © 2023 by Elsevier. Reprinted by permission of Elsevier.

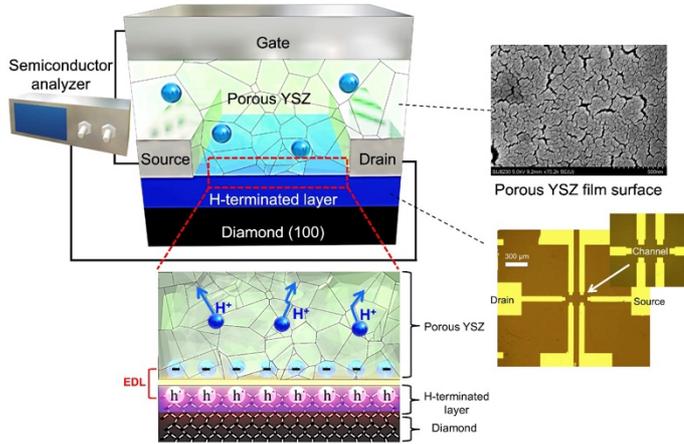


**Fig. 6** Variation in  $1/\tau$  of  $\text{LiNbO}_3$  devices with  $\text{LiNbO}_3$  interlayer of various thicknesses.<sup>20</sup> Copyright © 2023 by Elsevier. Reprinted by permission of Elsevier.

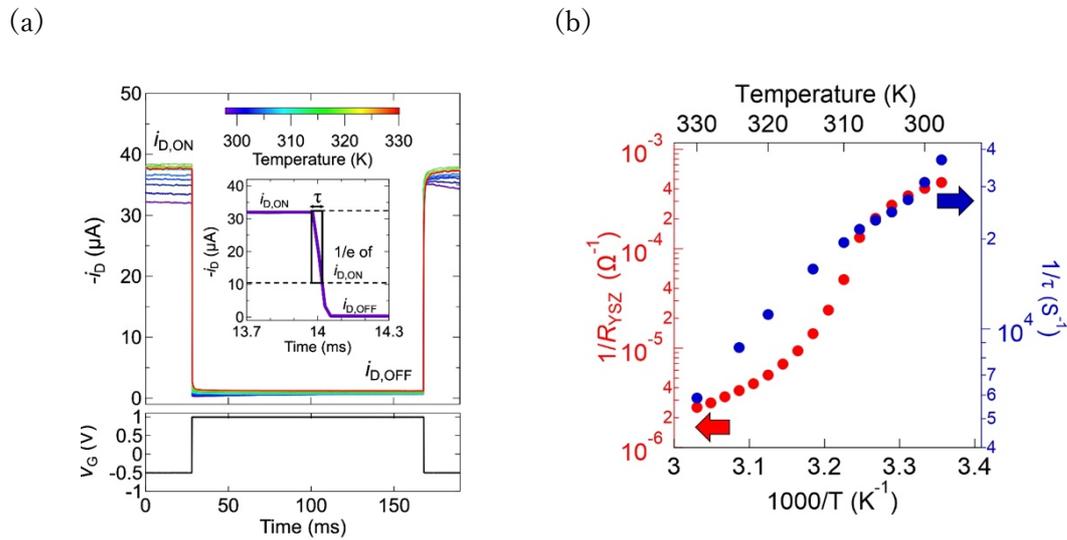
#### 4. Ultrafast charging-discharging dynamics of a porous proton conductor-based solid state electric double layer transistor

The electric double-layer effect has been observed not only in lithium-conducting solid electrolytes but also in proton-conducting solid electrolytes.<sup>14</sup> Nanograined or porous zirconia-based oxides are known to show high proton conductivity.<sup>21-25</sup> Y-stabilized zirconia (YSZ) thin films deposited by pulsed laser deposition at low substrate temperatures have fine nanopores and exhibit high proton conductivity due to the contribution of three types of water molecules (i.e., chemisorbed water, physisorbed water, hydrogen-bonded water) supplying proton conduction paths.<sup>26</sup> A schematic diagram of an electric double-layer transistor using this YSZ film and hydrogen-terminated diamond is shown in **Fig. 7**.<sup>14</sup> As in the case of the lithium solid electrolyte, the application of a  $V_G$  causes the transport of positively charged protons. It can control the electric double layer at the diamond/solid electrolyte interface. The drain current's response to applying a  $V_G$  pulse is shown in **Fig. 8(a)**.<sup>14</sup> Since the observed response is much faster than the other reported EDL devices, there was a doubt whether the response is due to proton conduction of YSZ, or dielectric property of YSZ. The temperature dependence of the relaxation time is shown in **Fig. 8(b)**.<sup>14</sup> In contrast to the case of lithium solid electrolytes, which speed up in the normal Arrhenius-type thermal activation with increasing temperature, a non-Arrhenius-type thermal activation is observed. This is in good agreement with the temperature-dependent character of proton conduction in the porous YSZ thin films,<sup>22,23</sup> supporting that the charge-discharge response of the electric double layer is closely related to the ion transport in the electrolyte regardless of the type of ionic carriers, not related to dielectric property of YSZ.<sup>27,28</sup> A comparison of the relaxation time at room temperature with previously reported electric double-layer transistors is shown in **Fig. 9**.<sup>14</sup>

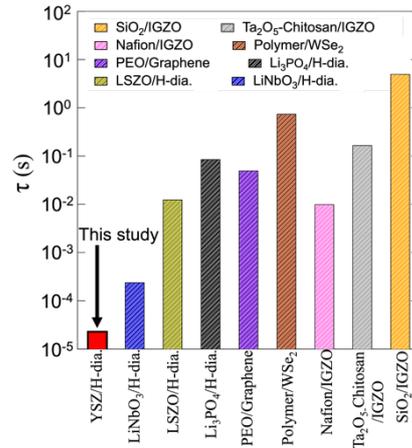
While standard electric double-layer transistors exhibit a relaxation time of more than 10 ms, the YSZ-type electric double-layer transistor exhibits by far the shortest relaxation time of 27  $\mu\text{s}$ , which is the highest speed of electric double layer transistors reported so far.<sup>13,20,29-32</sup>



**Fig.7** Illustration of hydrogen-terminated diamond-based electric double layer transistor comprised of porous YSZ thin film.<sup>14</sup> Reproduced from [14] by The Author(s) licensed under CC BY 4.0, published by Elsevier.



**Fig.8**(a) The  $i_d$  response to applying a  $V_G$  pulse. (b) The temperature dependence of the  $1/\tau$  and ionic conductivity of YSZ thin film.<sup>14</sup> Reproduced from [14] by The Author(s) licensed under CC BY 4.0, published by Elsevier.



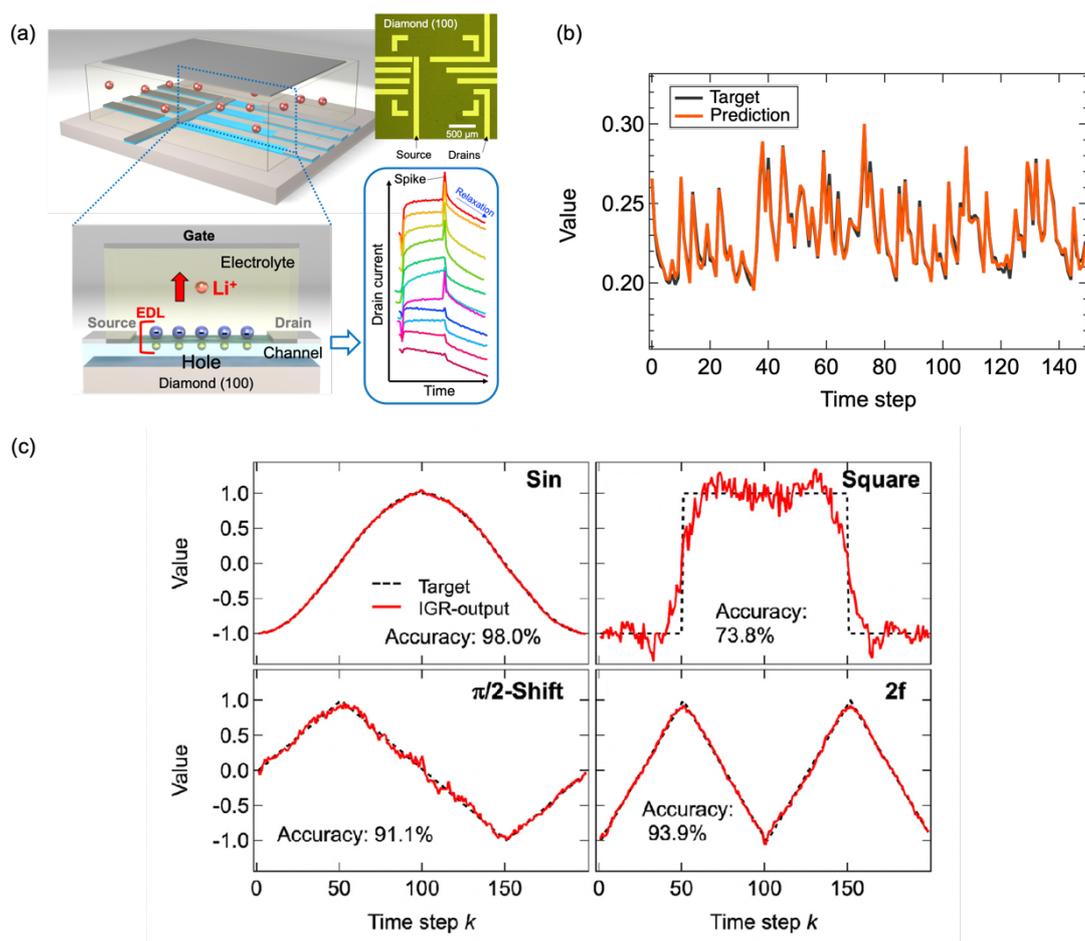
**Fig.9** The comparison of  $\tau$  with electric double layer transistors.<sup>13,14,20,29-32</sup> Reproduced from [14] by The Author(s) licensed under CC BY 4.0, published by Elsevier.

## 5. Application of solid state electric double layer transistors to neuromorphic computing

Energy consumption due to machine learning, such as deep learning and generative AI, has recently become a serious social problem. To solve this, neuromorphic devices that perform machine learning with high efficiency are needed. Among various methods, reservoir computing, which uses the non-linear responses of dynamical systems as computational resources for neuromorphic computing, has attracted particular attention.<sup>33-38</sup> This method can learn using far fewer computational parameters than general deep learning and thus can achieve low power consumption and high speed. To perform this reservoir computing with high performance, a dynamical system with strong non-linearity is required, and small devices are advantageous for highly integrated implementation in semiconductor devices. We have therefore attempted to apply the previously described solid electrolyte electric double layer transistor to reservoir computing.

**Figure 10(a)** shows a schematic diagram of an ion-gating reservoir, an electric double layer transistor specially designed for physically implementing reservoir computing.<sup>15</sup> Notable structural difference with typical electric double-layer transistors is that it has many drain electrodes useful for measuring various drain current responses from each semiconductor channel. The different channel lengths vary the transient drain current response. For specific information processing, time-series data is input to the ion-gating reservoir through the common gate electrode as a  $V_G$  pulse train, converted from the original time-series data. The applied  $V_G$  modifies the charge density of the electric double layer and the drain current, leading to transient drain current dynamics with a quasi-synaptic response featuring sharp

spikes and current relaxation with various speeds, as shown in **Fig. 10(a)**.<sup>15</sup> Despite the relatively simple transistor structure, the device generates such a complex current response. This is because the transport of ions in the electrolyte and the transport of electrons (holes) in the semiconductor channel affect each other near the electric double layer at the diamond/electrolyte interface, which can be termed ion-hole coupled dynamics.<sup>15</sup> The ion-gating reservoir's complex response was useful for performing reservoir computing. The performance was evaluated with a typical benchmark test of reservoir computing termed the 2<sup>nd</sup>-order nonlinear dynamic equation prediction task, a prediction task of time-series data. **Figure 10(b)** shows a comparison of the target waveform, which was generated by the 2<sup>nd</sup>-order nonlinear dynamic equation, and the prediction waveform, which was predicted based on the output of the ion-gating.<sup>15</sup> The prediction waveform generated by the ion-gating reservoir precisely reproduced the characteristic features of the target waveform. The ion-gating reservoir achieved the normalized mean square error, an indicator of the prediction performance, which is notably lower than the other reservoir computing devices, including spin torque oscillators and memristors.<sup>39,40</sup> The result evidenced the prominent performance of our ion-gating reservoir. The high performance was found to originate from a feature of the “edge of chaos” as a dynamical system, which is a dynamical state between order and chaos (disorder) and is known to be advantageous for information processing due to its high nonlinearity.<sup>41-44</sup> Similar physical reservoir computing can be performed by YSZ-based electric double layer transistor shown in **Fig. 7**.<sup>14</sup> **Figure 10(c)** shows target and output waveforms for nonlinear transformation task, performed with the YSZ-based ion-gating reservoir (IGR) operating with proton transport.<sup>14</sup> Various target waveforms (sine wave, square wave, phase shift wave, second harmonic wave) were successfully reproduced in the IGR-output waveforms from a triangle wave. The accuracy is higher than other reported physical reservoirs.<sup>42,45</sup> These results indicate a new direction of solid state ionics devices, including electric double layer and redox mechanisms.<sup>46-53</sup>



**Fig. 10.** (a) A schematic diagram of the ion-gating reservoir consisting of hydrogen-terminated diamond and  $\text{Li}^+$  solid electrolyte. (b) A comparison with target and prediction waveforms of the benchmark task (2nd-order nonlinear dynamic equation prediction task).<sup>15</sup> Reproduced from [15] by The Author(s) licensed under CC BY-NC 4.0, published by the American Association for the Advancement of Science. (c) A comparison with target and IGR-output waveforms of the nonlinear transformation task.<sup>14</sup> Reproduced from [14] by The Author(s) licensed under CC BY 4.0, published by Elsevier.

## 6. Conclusion

Field-effect transistors consisting of solid electrolytes and diamonds developed to investigate the electric double-layer effect of solid electrolytes and their application to state-of-the-art neuromorphic computing are outlined. The chemical stability of the diamond makes it suitable for investigating the electric double layer in ion-blocking conditions. Field-effect transistors containing this diamond and lithium solid electrolytes have been prepared, and electrical measurements, such as Hall measurements, have shown that the holes induced on the diamond surface by the electric double-layer effect are high density of around  $10^{13} / \text{cm}^2$

at the interface of lithium solid electrolytes such as LSZO,  $\text{Li}_3\text{PO}_4$  and  $\text{LiNbO}_3$ , but very low at the interface of LLTO.<sup>13</sup> These results indicate that the electric double-layer effect depends on the properties of the electrolyte; EELS measurements suggest that the suppression of the electric double-layer at the LLTO interface is due to the redox of Ti.<sup>13</sup> Furthermore, the charge-discharge rates of the electric double layer were investigated in detail for several electrolytes, with a difference of more than two orders of magnitude between  $\text{LiNbO}_3$  and  $\text{Li}_3\text{PO}_4$ .<sup>20</sup> It was suggested that these charge/discharge rates were very strongly influenced by a region of about 5 Å from the diamond surface. Electric double-layer transistors were also prepared using porous YSZ thin films exhibiting high proton conductivity, and the fastest response ever reported was obtained.<sup>14</sup> The transient response of the electric double-layer transistor was applied to reservoir computing, a type of neuromorphic computing, where the complex electrical response due to ion-electron coupled dynamics enables highly accurate time series data prediction tasks.<sup>15</sup> The interface formed by a solid electrolyte is a simple and very small area that can perform various functions such as energy storage and information processing.<sup>54</sup> These interfacial functions not only strongly depend on the properties of the bulk materials and thin films that constitute the interface but also have a high affinity with nanomaterials, including inorganic, organic, and low-dimensional materials designed by the Nanoarchitectonics principle, which has been reported extensively in recent years, and is expected to make dramatic developments based on materials science.<sup>55-60</sup> Establishing basic science on interfacial functions, including the electric double-layer effect, and actively applying it to a wide range of industries is of great importance in solving the severe problems we are facing this century, such as enormous energy consumption and information explosion.

### **Acknowledgments**

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