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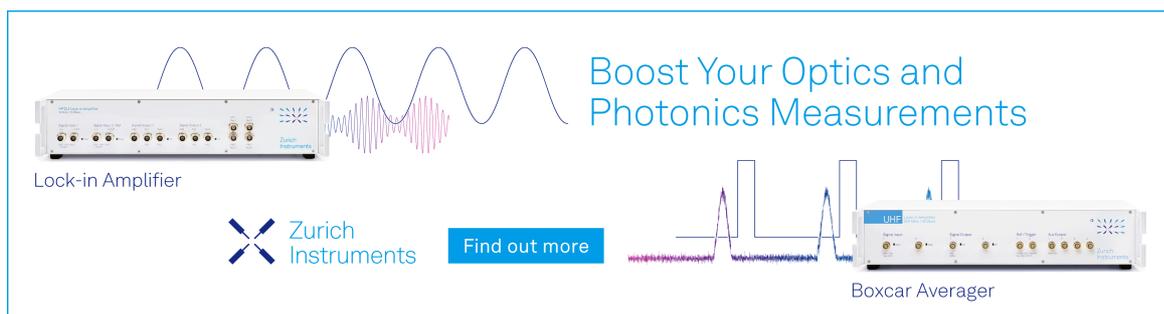
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ABSTRACT

In this study, we dry etched SiO₂-masked (001) β -Ga₂O₃ substrates in HCl gas flow at a high temperature without plasma excitation. The etching was done selectively in window areas to form holes or trenches with inner sidewalls of (100) and/or {310} facets, which are the smallest surface-energy-density plane and oxygen-close-packed slip planes, respectively. In particular, (100) faceted sidewalls were flat and relatively close to the substrate surface normal. Therefore, this simple dry etching method is promising for fabricating plasma-damage-free trenches and fins used for β -Ga₂O₃-based power devices.

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Monoclinic-structured β -Ga₂O₃ is a promising ultra-wide bandgap semiconductor for power electronics.¹ β -Ga₂O₃ has a high estimated critical electric field of ~ 8 MV cm⁻¹ (Refs. 2 and 3) and a decent electron mobility of ~ 200 cm² V⁻¹ s⁻¹,^{4–8} resulting in a high Baliga's figure of merit that outperforms that of conventional power semiconductors, such as SiC and GaN. Furthermore, the fabrication cost of β -Ga₂O₃ single crystals via melt growth^{9–12} is lower than that of SiC and GaN bulk crystals grown from vapor phases. Because of these two advantages over the competing semiconductors, β -Ga₂O₃ has emerged as a next-generation power semiconductor, thereby attracting attention among semiconductor research communities. Although the unipolarity of β -Ga₂O₃ can be an obstacle for device applications, promising device prototypes, such as Schottky barrier diodes (SBDs),^{13–17} metal-oxide-semiconductor field-effect transistors (MOSFETs),^{18–22} and modulation-doped field-effect transistors (MODFETs),^{23–27} have been demonstrated. Particularly, recently demonstrated fin FETs (FinFETs)^{28–33} and trench MOS-type SBDs (MOSSBDs)^{34–37} have exhibited excellent device performances, in which the fins and trenches aid in achieving normally off operation^{28,29,31–33} and high reverse breakdown voltages,^{34,36,37} respectively, by regulating the current flow in the confined regions without using p–n junctions.

Plasma-based anisotropic dry etching has been widely used to create such sophisticated device structures. Almost all β -Ga₂O₃ devices with fins/trenches reported to date have been fabricated using reactive ion etching (RIE) with chlorine-based chemistry, which is now virtually the de facto standard etching technique in

the β -Ga₂O₃ community.³⁸ However, the RIE process causes plasma damage on the processed surfaces, resulting in interface traps that degrade device performances due to limited effective channel mobility³¹ and a large hysteresis loop²⁸ in FinFETs and increased on-resistance in MOSSBDs.³⁵ To restore device performances, the plasma damage should be removed through wet treatments in acid or alkaline solutions,^{39–42} annealing,^{41,43} or self-reaction etching with gallium flux.⁴²

However, plasma-free anisotropic etching approaches have been explored to produce plasma-damage-free high-aspect-ratio structures. So far, various etching techniques, such as hot phosphoric acid etching,^{44,45} metal-assisted chemical etching (MacEtch),^{46,47} atomic gallium flux etching in an ultra-high vacuum environment,⁴⁸ and hydrogen environment anisotropic thermal etching (HEATE),⁴⁹ have been reported, where the strong anisotropic nature of the β -Ga₂O₃ crystal structure is more or less reflected in the etched structures. In terms of MacEtch, damage-free multiple fin channels were produced, and nearly zero-hysteresis operation of the FinFET was demonstrated.⁵⁰ However, the practical application of MacEtch is difficult because of its complicated etching system, in which etching proceeds very slowly (~ 100 nm/h) in a hydrofluoric acid solution under deep-UV illumination using a patterned Pt layer as a catalytic mask.⁴⁶ Additionally, the sidewall profiles of the MacEtch-formed fins on (010) substrates are positively tapered even when the fins are designed along vertical cleavage planes such as (100), indicating its weak anisotropy.⁴⁷ In contrast, HEATE is the most promising method among all non-plasma-based etching methods in terms of simplicity and

resulting etched profiles. HEATE is based on hydrogen-assisted thermal decomposition⁵¹ and has recently been reported by Kikuchi *et al.* for SiO₂-masked (010) β -Ga₂O₃ substrates.⁴⁹ They investigated the in-plane anisotropic etching behavior to find that (100) and {10 $\bar{1}$ } facets had the slowest and second-slowest lateral etching rates, respectively. Moreover, they succeeded in fabricating very high-aspect-ratio fins with (100)-faceted perfectly flat and vertical sidewalls without plasma damage that can be applied to distributed Bragg reflectors and nanofluidic channels. However, HEATE or the equivalent plasma-free dry etching method has not been applied to (001)-oriented β -Ga₂O₃ substrates, although most β -Ga₂O₃-based vertical power devices, including FinFETs and MOSSBDs, have now been fabricated on them.^{15–17,22,31–37} Therefore, it is essential to investigate a plasma-free dry etching on (001) substrates to fabricate fins/trenches for vertically structured β -Ga₂O₃-based power devices.

In our previous study, we demonstrated selective area growth using HCl-based halide vapor phase epitaxy (HVPE) on (010) and (001) β -Ga₂O₃ substrates to fabricate plasma-damage-free fins/trenches as a bottom-up growth method.⁵² In this growth system, the introduction of HCl etching gas in addition to the growth precursors was required for the suppression of parasitic gas-phase reaction and undesirable nucleation on the mask to achieve perfect selectivity. Furthermore, the excessive HCl gas supply led to selective area etching of the substrate in the window areas, which could be used as a plasma-free dry etching technique.

In this study, we investigated selective area HCl gas etching of (001) β -Ga₂O₃ substrates. Scanning electron microscopy (SEM) of the etched depressions revealed that the structures were dominated by (100) and {310} facets. In particular, (100) facets were smooth and free of plasma damage, although they were slightly inclined from the substrate normal. Therefore, gas-etched fins/trenches with faceted sidewalls can be applied to sophisticated power devices, including FinFETs and MOSSBDs.

We performed the HCl gas etching of β -Ga₂O₃ as follows. A circular-, radial-line-, stripe-, and square ($100 \times 100 \mu\text{m}^2$)-patterned SiO₂ masks (0.1- μm thickness) were prepared on (001) β -Ga₂O₃ substrates. The masks were fabricated via conventional photolithography. The details of the process are found in our previous study.⁵² The gas-phase etching was performed using a laboratory-made HCl-based HVPE system under atmospheric pressure. This system can directly supply a gas mixture of HCl (>99.999% pure) and N₂ (dew point < -110 °C) to the substrate.⁵³ In this study, an HCl/N₂ gas mixture with an HCl partial pressure of 63 Pa was supplied to the heated SiO₂-masked β -Ga₂O₃ substrate. Here, the substrate was vertically held at the center of the rotating holder in a horizontal quartz tube reactor, with the substrate surface perpendicular to the horizontal gas flow direction. Because the etching rate may strongly depend on the temperature, as verified in the H₂ etching experiments,⁵¹ we first examined etching rates of the (001) surface at the different reactor temperatures of 521, 750, 863, and 1038 °C by measuring the depths in the square windows ($100 \times 100 \mu\text{m}^2$) using a stylus profiler. The extracted etching rate monotonically increased with the temperature (Fig. S1), indicating that the rate can be controlled by the temperature. In this paper, we focused on the sample etched with the highest etching rate at 1038 °C and investigated its etching behaviors. We used SEM to characterize the etched structure's shape. Ga-focused ion milling was used to expose the cross section after depositing of a carbon surface protective layer. Atomic

force microscopy was used to observe the surface morphology of the etched (001) surface.

The HCl gas etching of β -Ga₂O₃ proceeded not only in the window area but also under the mask. When the acceleration voltage (V_{acc}) is high enough to allow primary electrons to pass through the mask, the trace of the under-etching can be observed using SEM from the surface side without removing the mask. Figures 1(a) and 1(b) compare SEM images of the same circular pattern observed at different V_{acc} s of 1 and 10 kV. When $V_{\text{acc}} = 1$ kV, only a part of the etched region was visible through the circular window [Fig. 1(a)]. However, when $V_{\text{acc}} = 10$ kV, an etched region under the mask was observed [Fig. 1(b)]. This high- V_{acc} condition allowed us to observe the outline of the etched depression and the relative positional relationship between the window edge and depression simultaneously, allowing us to measure the under-etching length [Fig. 1(c)]. Thus, to understand the etching behaviors, including under-etching features, we did not remove the mask and set V_{acc} to 10 kV when observing the etched structures from the surface side. Note that the etched structures without the mask were also observed with SEM after removing the mask

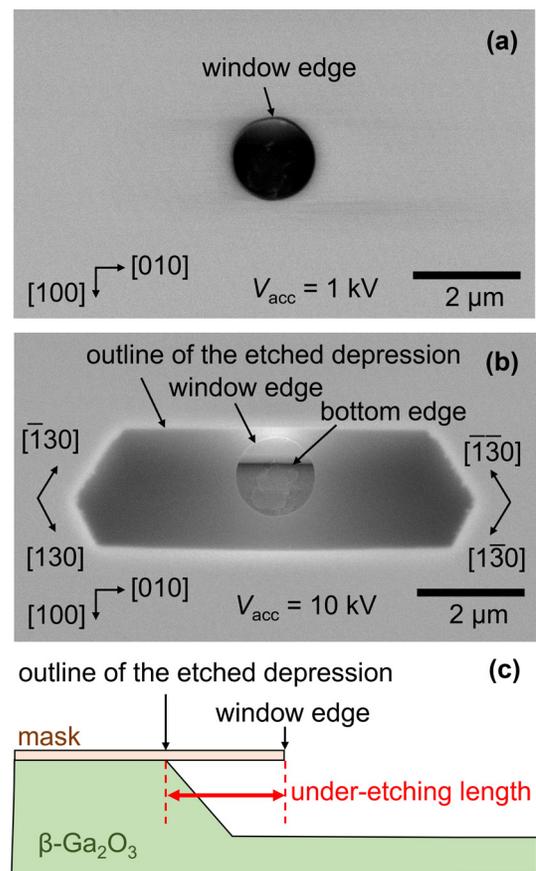


FIG. 1. Top-view scanning electron microscopy images of the etched trench on the same circular-patterned mask (1.7 μm in window diameter) recorded at (a) $V_{\text{acc}} = 1$ kV and (b) 10 kV. (c) Schematic cross section of the etched depression in the vicinity of the window edge, which illustrates the definition of under-etching length.

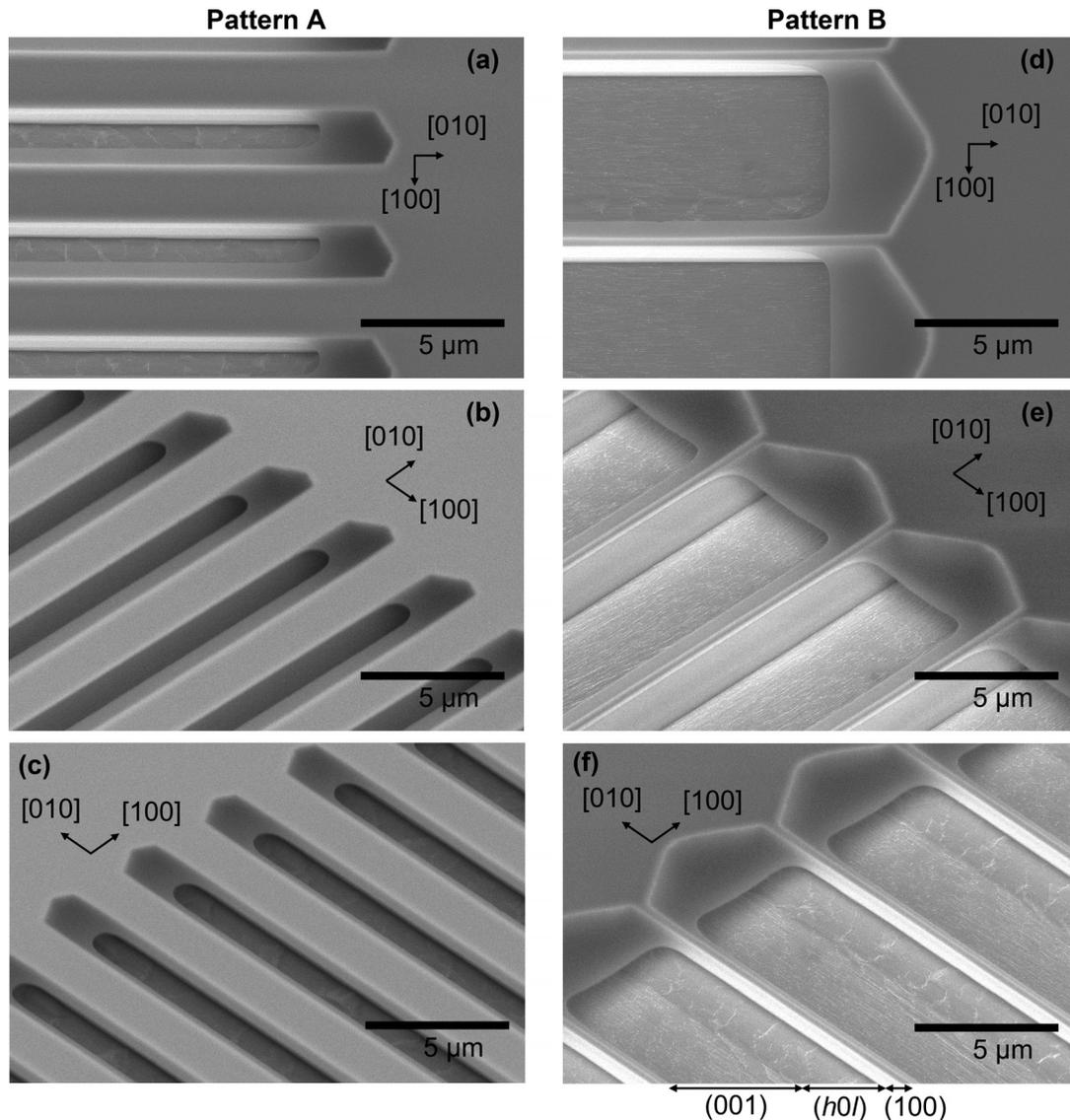


FIG. 3. Top- and tilted-view scanning electron microscopy images (0° and 50° from the surface normal, respectively) of etched trenches produced using the [010]-oriented stripe masks of (a)–(c) pattern A and (d)–(f) pattern B.

indicating that gas etching can fabricate not only trenches but also fins. Aside from these sidewalls, the surface morphology of the etched (001) bottom surface was measured using AFM, as shown in Fig. S6. The surface was relatively rough with the RMS roughness of 7.7 nm due to the presence of macro steps along [010] and byproducts deposited on the surface. Similar macro steps along [010] were observed for HVPE growth on (001) substrates,⁵² and, thus, the suppression of the formation of these steps should be a tough challenge. However, the deposited byproducts could be removed by optimizing the etching conditions, which should be our future work.

Cross-sectional observation of the trenches along [010] revealed more detailed information about the etched structures with (100)-faceted sidewalls. SEM images of the cross-sectional structures of the

trenches corresponding to patterns A and B are shown in Figs. 4(a) and 4(b), respectively. Schematic cross-sectional structures for both patterns are also shown in Fig. 4(c). Both sidewalls of the trenches have inclined (100) facets. The face angles between the (100) facets and surface (001) were measured to be 103° – 105° , which agrees with the lattice angle $\beta = 103.7^\circ$.⁵⁴ The inclination of the (100) facets could have caused uneven under-etching behavior between $[\bar{1}00]$ and $[100]$. The (100) facet should be formed almost immediately on the $[\bar{1}00]$ side because the plane was positively tapered. Therefore, the under-etching length on the $[\bar{1}00]$ side was very short because the etching rate was minimized soon after the (100) facet was formed. However, the negatively tapered (100) facet should take longer to form on the $[100]$ side because more crystal volume must be removed. The etching

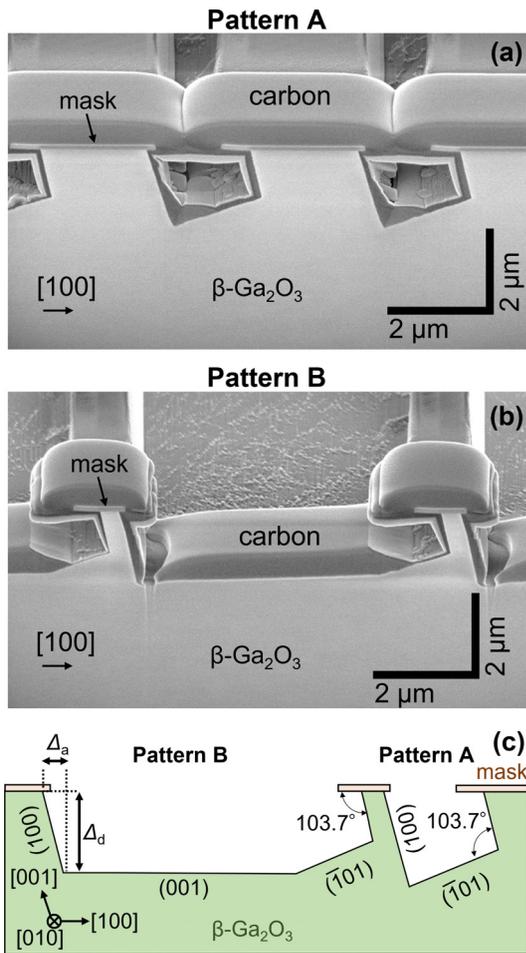


FIG. 4. (a) and (b) Tilted-view scanning electron microscopy images (54° from the surface normal) of cross-sectional structures of the trenches corresponding to Figs. 3(a)–3(c) (pattern A) and 3(d)–3(f) (pattern B). The carbon depositions near the cross sections are surface protective layers. Note that different scales should be used to measure vertical and lateral lengths on the cross sections. (c) Schematic cross sections for both patterns.

would proceed with planes other than (100) during the transitional period, and the etching rate should be faster than that of (100). Therefore, fast etching should occur for a longer time to increase the under-etching length on [100] side longer. The inclined planes at the bottom corners of the [100] side were identified as $(\bar{1}01)$ facets using cross-sectional SEM images. The measured face angles between the $(\bar{1}01)$ and the (100) facets were 100° – 102° , whose measured values were close to the calculated face angle of 103.7° . The emergence of the $(\bar{1}01)$ facet agrees with the fact that $(\bar{1}01)$ exhibited the next slowest etching rate to (100) among the planes belonging to the [010] zone based on the HEATE method.⁴⁹ Interestingly, the trench bottom was solely formed by the $(\bar{1}01)$ facet when the window width was small (pattern A) [Fig. 4(a)].

Using the geometry described in Fig. 4(c), the etching depth (Δ_d) can be calculated using the following relationship:

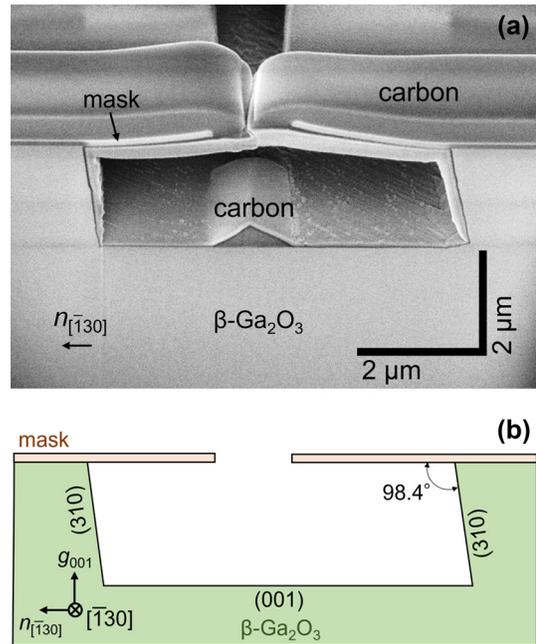


FIG. 5. (a) Tilted-view SEM images (54° from the surface normal) of a cross-sectional structure of the etched trench produced using the line-patterned mask ($1.2\ \mu\text{m}$ in window width) along $[1\bar{3}0]$, which are one of the radial lines in Fig. 2(b). (b) Schematic cross section corresponding to (a).

$$\Delta_d = \cot(\beta - 90^\circ)\Delta_a \sim 4.1\Delta_a,$$

where Δ_a is the projected length of the inclined (100) facet on (001), which can be measured using top-view SEM. For instance, Δ_a is measured to be $0.47\ \mu\text{m}$ from the top-view SEM image for the narrow window pattern A [Fig. 3(a)], and Δ_d is extracted to be $1.9\ \mu\text{m}$. This value agrees with the observed depth of $\Delta_d = 1.8\ \mu\text{m}$ obtained from the corresponding cross-sectional SEM image [Fig. 4(a)]. Given that such narrow trenches cannot be probed with a stylus or cantilever, the depth estimation method described above is very useful.

We also characterized the cross-sectional structure of an etched trench with {310}-faceted sidewalls. As discussed above, {310} facets had the second slowest lateral etching rate for forming faceted trench sidewalls. Thus, the cross-sectional structure of the trench with {310}-faceted sidewalls is worth investigating. Figure 5(a) shows a cross-sectional SEM image of the trench formed by the etching through a line-shaped window along $[1\bar{3}0]$, which is one of the radial lines shown in Fig. 2(b). A schematic of the cross section is shown in Fig. 5(b). The trench profile was defined solely by inclined (310)-faceted sidewalls and a (001)-faceted bottom. The cross-sectional profile is simpler than that of the [010]-oriented trench made using the pattern B, which has (100)-faceted sidewalls and an additional (101) facet at the bottom corner (compare Figs. 4 and 5). The measured face angles of the (310) facets from the surface were 98° – 99° , which were very close to the calculated angle of 98.4° . However, the (310) sidewalls were relatively rough due to the presence of macro steps, as evidenced by the SEM image recorded after removing the mask (Fig. S5). The generation of macro steps could be caused by the emergence of (100)

facets. Therefore, the (310) faceted sidewalls are unfavorable for practical fin/trench applications.

In conclusion, we used plasma-free HCl gas etching on a (001) β -Ga₂O₃ substrate to investigate its potential as a fin/trench fabrication process. After systematic characterization of the etched structures, we concluded that the direction of stripe windows should be along [010] to fabricate fins and trenches with faceted sidewalls of (100). Although (100) facets are inclined from the substrate's normal by 13.7°, their smooth and plasma-damage-free sidewall surfaces can improve the performances of fin/trench devices on (001) substrates. We consider such facet-formation-based fin/trench fabrication methods by plasma-free dry etching, and our previously proposed selective area growth will contribute to the development of the fabrication process of β -Ga₂O₃ devices.

See the [supplementary material](#) for the etching rate of (001) surface as a function of reactor temperature (Fig. S1), etched structures after removing the SiO₂ mask (Figs. S2–S5), and surface morphology of the etched (001) surface characterized by AFM (Fig. S6).

The preparation of the SiO₂ patterned mask and the characterization of the etched structures were performed at the Namiki Foundry and the Nanofabrication Facility (Project No. 22NM5110) in the National Institute for Materials Science.

AUTHOR DECLARATIONS

Conflict of Interest

The authors have no conflicts to disclose.

Author Contributions

Takayoshi Oshima: Conceptualization (equal); Methodology (equal); Writing – original draft (lead). **Yuichi Oshima:** Conceptualization (equal); Methodology (equal); Writing – review & editing (lead).

DATA AVAILABILITY

The data that support the findings of this study are available from the corresponding author upon reasonable request.

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